

Notice of References Cited	Application/Control No. 09/703,430	Applicant(s)/Patent Under Reexamination CHO ET AL.	
	Examiner Frank Duong	Art Unit 2666	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6,466,886	10-2002	Marmur, Oren	702/126
	C	US-6,631,144	10-2003	Johansen, Henrik Ingvar	370/516
	D	US-6,526,109	02-2003	Chang et al.	375/371
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

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	U	Belot et al, A 3.3-V Power Adaptiver 1244/622/155 Mbit/s Transceiver for ATM, SONET/SDH, IEEE pages 1047-1058, 1998
	V	Seo et al, A 3V Low Power 156/622/1244 Mbps CMOS Parallel Clock and Data Recovery Circuit for Optical Communications, IEICE, pages 1720-1727, 2000.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.